

**Name:** Dr. J P Patra  
**Address for Communication:** House No A4, Aditya Vihar  
Mahaveernagar, Raipur  
Chhattisgarh-492001  
**Date of Birth:** 02.11.1981  
**Mobile :** 91-8319527740  
91-9522219109  
**Email:** patra.jyotiprakash@gmail.com



**Career Objective:**

Dynamic instructor and tough leader focused on providing the student with a rigorous and challenging education, along with the confidence, tools, and skill require building and advancing toward a framework of success. Shape students through innovative coursework and stimulating projects formulated on personal decisiveness, business best practices, emerging business trends, and use of technology. Finally, believe in the statement that “The mediocre teacher tells. The good teacher explains. The superior teacher demonstrates. The great teacher inspires.”

**Teaching Experience : 14.6 Years**

**University and Institute Credential:**

- **Approved Professor** in Department of Computer Science & Engineering under statute 19 for Chhattisgarh Swami Vivekananda Technical University, Bilai.
- **Member of Syllabus Revision Committee** for Department of Computer Science & Engineering in Chhattisgarh Swami Vivekananda Technical University, Bilai
- Overall in charge for **NBA Accreditation** work in CSE Department atSSIPMT, Raipur and DIMAT Raipur

**Present Position: Professor & Head,** Department of CSE, SSIPMT, Raipur (Chhattisgarh)

**Educational Credentials:**

**Ph.D., Computer Science and Engineering**

MATS University, Raipur (C.G.) India.

**Year:** March 2015

**Thesis Title:** Performance Improvement of a Dynamic System Using Soft Computing Approaches

**M.E., Computer Science and Engineering (CTA)**

**University:** CSVTU, Bhilai (C.G.) India.

**Year:** August 2008

**Marks:** 77% (First Division with Honors)

**B.E, Computer Science and Engineering**

**University:** BPUT, Odisha, India.

**Year:** July 2004

**Marks:** 67 % (First Division)

**Professional Experience:**

- Currently working as a Professor & Head in Computer Science & Engineering department at Shri Shankaracharya Institute of Professional Management & Technology, Raipur (C.G) since 15.06.2012
- Previously working as an Associate Professor and Head of Department in Computer Science & Engineering at Kruti Institute of Technology & Engineering, Raipur (C.G) since Sep 2011 to June 2012.
- Previously working as a Reader in CSE Department at Disha Institute of Management & Technology, Raipur from Feb.2009 to August 2011.
- Previously Working as a Lecturer in Computer Science & Engineering Department at Shri Shankaracharya College of Engineering & Technology, Bhilai from Nov.2004 to Feb 2009

**Achievements:**

**Authorship of Book**

1. **“ANALYSIS & DESIGN OF ALGORITHMS”** (B.E 5TH Semester CSE) is published under SUN INDIA PUBLICATION, NEW DELHI, **ISBN-978-93-80674-53-7**

2. **“Performance Improvement of a Dynamic System Using Soft Computing Approaches”** LAP LAMBERT Academic Publishing AG & Co. KG Saarbrücken, Dudweiler Landstraße 28, 66111 Saarbrücken Germany.(**ISBN :978-3-659-82968-0**)

- Participated in **DELHI DOORDARSHAN, RAIPUR** telecast program **“LOKMANCH”** on topic **“E-Waste”** which was telecast on 14<sup>th</sup> JULY, 2015.
- Remote Centre Coordinator for **IIT BOMBAY and IIT KHARAGPUR** (RCID-1240) for a Project initiated by A Govt. of INDIA and MHRD to teach 10,000 Teachers Simultaneously under a Mission called “Empowerment of Students and Teachers through Synchronous & Asynchronous Instruction”.

- Team Leader for E-Yantra Sponsored project under taken by **IIT BOMBAY**
- Successfully Completed 15 workshops for **IIT BOMBAY and IIT KHARAGPUR** and achieved FOUR STAR Rating for Remote Centre.
- **30** Papers published in International Journals (SCOPUS, UGC APPROVED) with Good Impact Factors
- **21** papers presented in International conference, National Conference & seminars.
- **AAKASH TABLET Project Coordinator under AAKSH TABLET Program for IIT BOMBAY initiated by MHRD, INDIA.**
- **Elected as Chairman for Raipur Chapter (Division-IV), Computer Society of India.**
- Good Academic Credentials: M.E. (Honors) with specialization in Computer Technology & Application.
- The paper Entitled “Minimized Time Complexity & Memory Efficiency of Strassen’s Matrix multiplication using basic Linear Algebra Subprograms” was presented and awarded Third Best Paper in the National Conference on “Application of computer Technology for Rural development in India”, held during 3rd to 4th July 2010, at CIT, Rajnandgaon.

**Member in Editorial Board and Reviewer Body for International Journals:**

- Advances in Science, Technology and Engineering Systems Journal (ASTESJ) (ISSN: 2415-6698)
- International Journal of Computer Science and Artificial Intelligence(IJCSAI)
- International Journal of Computer Science & Communication Networks Issues (IJCSCN)
- International Journal of Computer Trends and Technology(IJCTT)
- International Journal of Scientific and Engineering Research (IJSER)

**Subject Under Taken for PG and UG Courses:**

- Subject under taken for B.E. Course: Analysis & Design Of Algorithms, Data Structures, Artificial Intelligence & Expert Systems, Operating System, Cryptography and Network Security, JAVA, C, C++,
- Subject under taken for M.E/M.Tech Course: Analysis & Design of Algorithms, Cryptography and Security threats, Data Encryption & Compression Techniques

**Administrative:**

Undertake administrative appointments in the college for conduct of academic curriculum and administrative tasks as required in running of University such as

1. In charge of **Incubation Centre** and **Centre of Excellence**
2. In charge for CG STARTUP under Make in Chhattisgarh Concept
3. Program Coordinator (Computer Science & Engineering)
4. Board of Studies Member Dept of Computer & Engineering since 2010-11.
5. Honorary Chairman of Computer Society of India Raipur Chapter
6. Project In-charge- For B. E Computer Science & Engineering
7. Training Seminar In-charge of B.E Students.
8. Class Advisor for B.E Course
9. Departmental Examination In-charge
10. Member of organizing committee of various National & International Conference

**Area of Research**

1. Soft Computing
2. Algorithm Analysis & Design
3. Image Processing & Cryptography

Number of Projects Guided (Undergraduate Level)-50

Number of Projects Guided (Postgraduate Level)-05

Number of PhD Scholar - 02\*

**Skill Set:**

Languages / Packages: C, C++, Java, PL/SQL, MATLAB, Prolog

Technologies: Servlet 2.4, JSP 2.0, JNDI 1.2.1, RMI, JDBC 3.0.

Operating System: Windows, UNIX

**Short Term Training Programs & Staff Development Program:**

<b>STTP/FDP/Training Activity</b>	<b>Title</b>	<b>Duration</b>	<b>Remark</b>
STTP	Programme Accreditation by NBA for Technical Institutions	One Week	Conducted by NITTR, Chandigarh
Training Activity	Training for Trainers on Quality Concepts and Problem Solving Techniques	4 Days	Conducted by QCFI , Bhilai Chapter
STTP	Introduction to Structural Engineering	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT KHARAGPUR
STTP	Engineering Physics	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT BOMBAY
STTP	Environmental Studies	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT BOMBAY
STTP	Introduction to Design of Algorithms	Two Week	2Week ISTE STTP Conducted by MHRD and IIT KHARAGPUR
STTP	Control System	Two Week	2 Week ISTE FDP Conducted by MHRD and IIT KHARAGPUR
STTP	Fluid Mechanics	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT KHARAGPUR
STTP	Computer Programming	Two Week	2Week ISTE STTP Conducted by MHRD and IIT BOMBAY

Curriculum Vitae Of Prof. Dr. J P Patra

STTP	Signal and System	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT KHARAGPUR
STTP	Engineering Mechanics	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT BOMBAY
STTP	Analog Electronics	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT KHARAGPUR
STTP	DataBase Management System	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT BOMBAY
STTP	Aakash Android Application Programming	4 Days	4Days ISTE STTP Conducted by MHRD and IIT BOMBAY
STTP	Engineering Thermodynamics	Two Week	2 Week ISTE STTP Conducted by MHRD and IIT BOMBAY
STTP	Aakash For Education	Two Days	2 Days ISTE STTP Conducted by MHRD and IIT BOMBAY
STTP	Open Source Application	One Week	AICTE-ISTE approved one week STTP
STTP	Computer Architecture and Design	Two Week	MHRD-AICTE FDP
STTP	Data Structures, Algorithms and Complexity	Two Days	Conducted by BIT Durg and Indian Association for Research in Computing Science

### **Professional Body Membership**

1. **Life Member of ISTE**(The world's largest technical professional organization for the advancement of technology)-LM114369
2. Associated with International **Association of Engineers** (IAENG) Member No: 141279
3. **Life Member of CSI** (Computer Society of India- I1510001) one of the oldest and finest body for Computer Science and Engineering field.

### **Certificate of Excellence:**

1. Certificate of Appreciation from KITE, Raipur for a significant contribution for the activities of teaching, administration and improvement under corporate Social Responsibility of the Institute.

### **International Journal & Conference:**

1. Dr. J. P. Patra, Linesh Gohil, Aman Yerpude. "Implementation of File Tagging System Using Android" *International Journal of Scientific Research in Science, Engineering and Technology*, 7(4), 37-42 ISSN: 2394-4099.
2. Dr. J. P. Patra, Amit Jain, Ajay Singh Thakur "Image Recognition using Machine Learning Application" *International Journal of Scientific Research in Science, Engineering and Technology*, 4(3), 167-171 ISSN: 2394-4099.
3. Dr. J. P. Patra, Snigdha Choudhury, Bhumika Joshi, "Comparison of Time complexity of Symmetric and Asymmetric Key Cryptographic Algorithms" *International Journal of Scientific Research in Science, Engineering and Technology*, 3, 162-166, ISSN: 2394-4099.
4. Dr. J. P. Patra, Ankit Jain, Rishabh Namdeo, "Arduino Based Sensor for Fuel tank Monitor" *International Journal of Scientific Research in Science, Engineering and Technology*, 4(3), 210-212, ISSN: 2394-4099.
5. Dr. J. P. Patra, Anee Dhawna, Ayush Jain, "Poor Quality Cost Estimation: Loss Estimation Due to Poor Quality Product" *International Journal of Scientific Research in Science, Engineering and Technology*, 7(4), 962-966, , ISSN: 2394-4099
6. Dr. J P Patra, Ashish Trivedi, Arpit Sharma "Vending Machine: A New Technology For Medicine Dispenser" in Journal of advanced research in dynamic control system (JARDCS), Issue (18), Pages 2756-2764 ISSN-1943-023X
7. Dr. J P Patra, Riju Bhattacharya, Sumitra Samal "An Innovative Technique to Convert and save Ancient Handwritten Documents in Word Files Using OCR and Neural Network" in Journal of advanced research in dynamic control system (JARDCS), Vol. 9, Issue (10), Pages 64-69, ISSN-1943-023X

8. Dr J P Patra , Nayan Shivhare, Shraddha Verma “Comparision and Assesment of Evolutionary and Parallel Genetic Algorithms for a Cryptarithmic Problem “ in (IJCSIT) International Journal of Computer Science and Information Technologies, Vol. 7 (2) , 2016, 503-506, ISSN:0975-9646
9. Dr J P Patra , Karishma Rathod, Udita Chauhan “ Implementation Of Information Security Using Object Oriented Concept” in IRACST - International Journal of Computer Science and Information Technology & Security (IJCSITS), ISSN: 2249-9555, Vol.6, No 2, Mar-April 2016
10. Dr Jyotiprakash Patra , Mr. Puru Agrawal “PureView Technology (Optimisation & Evaluation Techniques in Image Processing)” International Journal of Knowledge Based Computer Systems ISSN Number: 2321-5623, Volume 3, Issue 2, December 2015
11. Dr Jyotiprakash Patra “Classroom Attendance Application” International Journal of advanced studies in Computer Science and Engineering IJASCSE , ISSN: 2278- 7917, Volume 4, Issue 4, 2015
12. Dr Jyotiprakash Patra “CHATBOT ASSISTING: SIRI” International Journal of Advanced Engineering Research and Studies E-ISSN2249–8974, Int. J. Adv. Engg. Res. Studies/IV/II/Jan.-March,2015/102-103.
13. Dr Jyotiprakash Patra “An LSB Method Of Image Steganographic Techniques” Int. Journal of Engineering Research and Applications ISSN : 2248-9622, Vol. 5, Issue 4, ( Part -5) April 2015, pp.62-65
14. Dr Jyotiprakash Patra “CDN in Intra-Cloud using Smart phones” International Journal of Computer Trends and Technology (IJCTT) ,E-ISSN:2231-2803,P-ISSN:2349-0829, volume 22 Number 1–April 2015
15. Dr Jyotiprakash Patra “Android Application Development & Its Security” in International Journal of Computer Science and Mobile Computing, ISSN 2320–088X (Online), Vol.4 Issue.3, March- 2015, pg. 714-719.
16. Mr.Jyotiprakash Patra, Dr.Partha Sarathi Khuntia, “Performance Enhancement of a Dynamic System for Different Controller Using Soft Computing Methods” in International Journal of advanced studies in Computer Science and Engineering IJASCSE ,ISSN: 2278- 7917, Volume 3, Issue 4, 2014
17. Mr.Jyotiprakash Patra, Divya Balwani & Pooja Tandon, "The Novel Approach To Improve The Performance Of Examination For Technical Universities", International Journal of Research in Computer Engineering and Electronics (IJRCEE), ISSN (Online): 2319-376X, Volume 2 Issue 2, March-April 2014



18. Mr. Jyotiprakash Patra, Mrs. Sumitra Samal, & Mrs. Preeti Tuli, "Assessment of PSO and PCA Algorithms for Face Recognition Using Different Performance Index Factors" in International Journal of Engineering Research & Technology (IJERT), ISSN: 2278-0181, Vol. 2 Issue 9, September – 2013.
19. Mr. Jyotiprakash Patra, Dr. Partha Sarathi Khuntia, & Mrs. Sumitra Samal "Analysis and Comparison of different performance index factor for Conventional PID Controller" in International Journal of Emerging Technologies in Computational and Applied Sciences (IJETCAS), ISSN (Online): 2279-0055, ISSN (Print): 2279-0047 Issue 4, Volume 3, pp. 242-250, March-May, 2013
20. Mr. Jyotiprakash Patra, Mrs. Sumitra Samal, & Mrs. Preeti Tuli "Analysis and Comparison of Advanced Face Detection Using PSO and PCA" in International Journal of Emerging Technologies in Computational and Applied Sciences (IJETCAS), ISSN (Online): 2279-0055, ISSN (Print): 2279-0047 Issue 4, Volume 3, pp. 293-298, March-May, 2013
21. Jyotiprakash Patra, Partha Sarathi Khuntia, Sumitra Samal, (2012) A Novel Approach to Optimize System Response of a PID Controller, International Journal of Computer Science and Information Technology and Security, Vol. 2, No. 5, October 2012. 1021, ISSN: 2249-9555
22. Mr. Jyotiprakash Patra, & Dr. Partha Sarathi Khuntia "Performance Enhancement of a Dynamic System Using PID Controller Tuning Formulae" in IJCSI International Journal of Computer Science Issues, ISSN (online): 1694-0814, Vol. 8, Issue 5, No 3, Page 342-347, September 2011.
23. Mr. Jyotiprakash Patra, & Mr. Somesh Dewangan "Security Enhancement of Dynamic System Using PID Controller and Optimization Algorithm" in IJCSE International Journal on Computer Science and Engineering, ISSN: 0975-3397, Vol. 4, No 8, August 2012.
24. The paper Entitled "A Model for Secured Requirements: Threat Model Perspective" authorized by Mr. Jyoti prakash Patra, & Mr. Somesh Dewangan was presented and published in the 2nd International Conference IT & BUSINESS INTELLIGENCE (ITBI-10) held during November, 12-14, 2010 at IMT, Nagpur. The ISBN NO: 978-81-7446-900-7.
25. The paper Entitled "PID Controller Tuning Formulae for Improvement of Transient and Steady State Performance" authorized by Mr. Jyoti prakash Patra, & Dr. Partha Sarathi Khuntia was presented and published in the 3rd International Conference IT & BUSINESS INTELLIGENCE (ITBI-11) held during November, 25-27, 2011 at St. Ann's College for Women, Hyderabad.
26. The paper Entitled "Analysis of Cluster Based Routing Algorithms in wireless Sensor Networks" authorized by Mr. Jyoti prakash Patra, Mr. Siddharth Choubey & Mrs. Abha choubey was presented and published in the Conference SENNET-

09 held during Dec 8-10, 2009 at VIT University, Vellore. The paper Code is SNE169.

27. Participated in First International Symposium on topic "CYBER SECURITY , CLOUD COMPUTING AND MACHINE LEARNING" held on 14<sup>th</sup> to 15<sup>th</sup> December 2012 as faculty CSE organized by IET, ITM University, Raipur.
28. The paper Entitled "An Innovative Technique to Convert and save Ancient Handwritten Documents in Word Files using OCR and Neural Network" authorized by Dr. J P Patra, Mr. Riju Bhattacharya, Mrs. Sumitra Samal was presented and published in International Conference (ICI-SD-SMT2017) held during 25-26<sup>th</sup> March 2017, atSSIPMT, Raipur
29. The paper Entitled "Automated Medicine Dispenser Vending Machine Using Acrylic and Arduino Board" authorized by Dr. J P Patra, Ashish Trivedi, Arpit Sharma was presented and published in International Conference (ICI-SD-SMT2017) held during 25-26<sup>th</sup> March 2017, atSSIPMT, Raipur
30. The paper Entitled "iLocate: A Pioneering App to track a Lost Mobile" authorized by Dr. J P Patra, Amrita Bhatti, Komal Kaur Gill was presented and published in International Conference (ICI-SD-SMT2017) held during 25-26<sup>th</sup> March 2017, atSSIPMT, Raipur

**National Conference & Seminar:**

1. Participated in Two Days National Conference on "Research and Development in Computer Science and Application" from 20<sup>th</sup> to 21<sup>st</sup> February, 2015 at BIT, Durg.
2. The paper Entitled "PID Controller Tuning Formulae for a Dynamic System" authorized by- Mr. Jyotiprakash patra & Dr. Partha Sarathi Khuntia was presented in the National Conference "Frontiers of Communication and Instrumentation", held during 11<sup>th</sup> & 12<sup>th</sup> November 2011, atTEHNO INDIA CAMPUS, Salt Lake, Kolkata.
3. The paper Entitled "Neural Networks in Data Warehousing and Mining" authorized by- Mr. Jyotiprakash patra was presented in the National seminar organized by Department of Applied Mathematics" held during 8<sup>th</sup> & 9<sup>th</sup> July 2010, at BIT, Durg.
4. The paper Entitled "RSA Digital Signature" authorized by- Mr. Jyotiprakash patra was presented in the National Conference organized by Dept. of CSE, held during 4<sup>th</sup> February 2012, at KITE, Raipur.
5. The paper Entitled "Minimized Time Complexity & Memory Efficiency of Stassen's Matrix multiplication using basic Linear Algebra Subprograms" Jyotiprakash patra & Somesh Dewangan in the National Conference on "Application of computer Technology for Rural development in India", from 3<sup>rd</sup> to 4<sup>th</sup> July 2010, atCIT, Rajnandgaon.

6. The paper Entitled “Threat avoidance model for Network security” by Mr. Jyotiprakash patra & Ms. Asmita Yadav in the National Conference on “Application of computer Technology for Rural development in India”, from 3<sup>rd</sup> to 4<sup>th</sup> July 2010, at CIT, Rajnandgaon.
7. The paper Entitled “Application of Bio Matrix” authorized by Mr. Jyotiprakash Patra & Ms. Monika Yadav was presented on “Recent Trends in Distributed Intelligence System & Security Advancements, during 26<sup>th</sup> – 27<sup>th</sup> March, 2010 at LNCT, Bhopal.
8. The paper Entitled “Fundamentals of Crypto-Systems” authorized by Mr. Jyotiprakash Patra was presented on “Recent Trends in Distributed Intelligence System & Security Advancements, during 26<sup>th</sup> – 27<sup>th</sup> March, 2010 at LNCT, Bhopal.
9. The paper Entitled “New Physical Attack & Security of smartcard” authorized by Mr. Jyotiprakash Patra was presented on “The National Conference in Emerging Technologies” conducted during 13<sup>th</sup> – 14<sup>th</sup> March 2009 at MPC CET, Bhilai.
10. The paper Entitled “Bluetooth Security” authorized by Mr. Jyotiprakash Patra & Mr. Rajesh Tiwari was presented on “Advanced communication techniques & Networking: Exploring Challenges” conducted during 6<sup>th</sup> – 7<sup>th</sup> January 2007.
11. The paper Entitled “Intrusion Detection System in Manet & Recent Trends” was presented on “Techno vision: A National Level Technical Conference” conducted during 26<sup>th</sup> – 27<sup>th</sup> October 2007 at SSCET, Bhilai.
12. The paper Entitled “A Threat Model Perspective: Secure Requirements” authorized by Mr. Jyotiprakash Patra & Mr. Somesh Dewangan was presented on “Unleashing the Power of Excellence” at DIMAT, Raipur.

#### **Workshop & Seminar:**

1. Participated in MISSION 10X ENGINEERING FACULTY WORKSHOP from 4<sup>th</sup> to 6<sup>th</sup> September, 2013 at SSIPMT, Raipur.
2. Participated in MATLAB Workshop from 26<sup>th</sup> to 27<sup>th</sup> November, 2012 at SSIPMT, Raipur.
3. Participated in Two days National Workshop on “Object Oriented Design Pattern” & “Agile Boot Camp” on 15<sup>th</sup> & 16<sup>th</sup> September, 2012 at SSIPMT, Raipur
4. Participated in Workshop on “Secure and Integrated Enterprise Applications” on 18<sup>th</sup> & 19<sup>th</sup> March 2011 at DIMAT, Raipur.

5. Participated in Workshop on “Research Avenues of Digital Image Processing in context to Medical Imaging” on 30<sup>th</sup> & 31<sup>st</sup> March 2011 at SSCET organized by CSI, Chennai.
6. Participated in International workshop by INNS on “FUTURE DIRECTIONS OF ANN (FDANN10)” organized by IMT, Nagpur on 12<sup>th</sup> November 2010.
7. Participated in Two days Seminar on “Modern Trends in Applied Mathematics towards the Application of Fuzzy Logic” on 8<sup>th</sup> & 9<sup>th</sup> July, 2010 at BIT, Durg.
8. Participated in Workshop on Virtual Instrumentation through “Lab view” held on 3<sup>rd</sup> Dec 2008 organized by IIPC& Dept. of ETE, SSCET, Bhilai.
9. Participated in All India Seminar on “Advanced Communication Techniques and Networking : Exploring Challenges” on 6<sup>th</sup> to 7<sup>th</sup> January 2007 at SSCET, Bhilai
10. Participated in Workshop on “OrCAD” organized by SSCET, Bhilai held in Jan 2005.

**.Professional References:**

<b>Name</b>	<b>Current Position</b>	<b>Organization</b>	<b>Contact Details (E mail ID, Mobile No., Phone no.)</b>
Dr. Deepak B Phatak	Professor	IIT BOMBAY	E mail ID: dbp.it.iitb.ac.in Phone no. 02225767747(O), 02225768747(R)
Dr. Partha Sarathi Khuntia	Principal	KIST, Bhubaneswar	E mail ID: parthasarathi_k@yahoo.com M-9178364168
Dr. A S Umesh	Director	TIT BHOPAL	E mail ID: umeshasp@yahoo.co.in M-09685246981

**Declaration**

I hereby declare that the information given above is true to the best of my knowledge.

**Date:**

**Place:** Raipur

**(Dr. J P Patra)**